

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S46	1	"10/396118"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S47	0	"x-lfsr" and "y-lfsr"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S48	0	"x-lfsr" "y-lfsr"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S49	360	lfsr and cdma	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S50	0	10/651848	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S51	51	(first adj initial adj state) and (second adj initial adj state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S52	0	(first adj initial adj state) and (second adj initial adj state) and (lfsr or (linear adj feedback adj shift adj register)) and (cdma or wcdma or 3gpp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S53	0	(first adj initial adj state) and (second adj initial adj state) and (lfsr or (linear adj feedback adj shift adj register)) and (cdma or wcdma)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08

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S54	99	(6721293 6795689 20040120289 6501788 20030099357 6836469 20030081575 20040120274 20060121907 20030095529 20040258182 6804214 7035676 20010034254 20030039303 20030119444 20040032848 20040085921 20050085255 20050094816 20050143118 6570889 6862314 6775318 6934526 20020009129 20020051431 20020064211 20030103478 20030133429 20040057468 20050063345 20060193339 6185244 6459694 7061967 20030235238 20040114552 20060056552 5930366 5956368 6301289 6339646 6385264 6526039 6526065 6526091 6535495 6567482 6577671).pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S55	218	(lfsr or (linear adj feedback adj shift adj register)) and (cdma or wcdma or 3gpp) and (qam or (i near q))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S56	4	(first adj initial adj state) and (second adj initial adj state) and (lfsr or (linear adj feedback adj shift adj register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S57	0	(lfsr or (linear adj feedback adj shift adj register)) and (cdma or wcdma or 3gpp) and (qam or (i near q)) and (new adj initial adj state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S58	51	lfsr and wcdma	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S59	0	(lfsr or (linear adj feedback adj shift adj register)) and (cdma or wcdma or 3gpp) and (qam or (i near q)) and (initial adj state) and DSC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S60	19	lfsr and wcdma and (QAM or ( I near Q))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08

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S61	82	(lfsr or (linear adj feedback adj shift adj register)) and (cdma or wcdma or 3gpp) and (qam or (i near q)) and (initial adj state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S62	551	(lfsr or (linear adj feedback adj shift adj register)) and (cdma or wcdma or 3gpp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S63	1	(lfsr or (linear adj feedback adj shift adj register)) and (cdma or wcdma or 3gpp) and (qam or (i near q)) and (first adj initial adj state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S64	26	(lfsr or (linear adj feedback adj shift adj register)) with x with y	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S65	9	(lfsr or (linear adj feedback adj shift adj register)) with x with y and (wcdma or cdma or 3gpp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S66	0	(lfsr or (linear adj feedback adj shift adj register)) with x with y and (wcdma or cdma or 3gpp) and dsc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S67	212	secondary with (scrambling adj code)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S68	4573	(lfsr or (linear adj feedback adj shift adj register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08

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S69	14	S67 and S68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S70	2	(lfsr or (linear adj feedback adj shift adj register)) with x and (new with (initial adj state))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S71	0	(lfsr or (linear adj feedback adj shift adj register)) with x and new adj initial adj state	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S72	75	(lfsr or (linear adj feedback adj shift adj register)) and (new with (initial adj state))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S73	3703	375/130	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S74	1	(lfsr or (linear adj feedback adj shift adj register)) and new adj initial adj state	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S75	7454	370/342	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S76	5	S73 and S72	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08

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S77	6	S75 and S72	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S78	54	S75 and S67	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S79	8	secondary with (scrambling adj code) and (initial adj state)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S80	16	S73 and S67	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S81	2312	375/147	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S82	19	S81 and S67	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S83	0	S81 and S72	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S84	233	708/252	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08

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S85	0	((lfsr or (linear adj feedback adj shift adj register)) and new adj initial adj state).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S86	4	S84 and S72	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S87	3	S84 and S67	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S88	2	((lfsr or (linear adj feedback adj shift adj register)) and (new with (initial adj state))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S89	0	(second adj (lfsr or (linear adj feedback adj shift adj register)) and (new with (initial adj state))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:08
S90	3	"4217469".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/21 14:13

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... a finite number of steps from its **first initial state** to a **second initial state**, ... the **code generator** of the receiving apparatus must necessarily be ...

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Results 1 - 10 of about 101 for "**code generator**" "**first initial**" "**second initial**". (0.75 seconds)

Method of producing rolling code and keyless entry apparatus using ...  
changing the **first initial** code variable by producing at least a **second initial** code variable  
and transmitting the at least **second initial** code variable to ...  
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Iterative CDMA phase and frequency acquisition - US Patent 7050485

... wherein the acquisition subsystem includes: a **code generator** that is ... The method of  
claim 14, wherein the **second initial** frequency corresponds to the ...  
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**second initial** load digit and so forth until. n. digits have been. determined. The local **code generator** feedback loop. is now. closed and shifting begins. ...  
[ieeexplore.ieee.org/iel5/8159/23795/01091714.pdf](http://ieeexplore.ieee.org/iel5/8159/23795/01091714.pdf) - [Similar pages](#)

Time **code generator** - Patent 5892552

A time **code generator** is provided and contains a detector, ... interfacing means can  
change at least one of said **first initial** value and said **second initial** ...  
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Iterative CDMA phase and frequency acquisition - Patent 7050485

The method of claim 14, wherein the **second initial** frequency corresponds to ... The code  
sequence is provided to the correlator 320 by a **code generator** 310 ...  
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EP1496637 Samsung european software patent - Apparatus and method ...

A CRC (Cyclic Redundancy **Code**) **generator** is a typical example of the error ... The **first initial** value and the **second initial** value are determined within a ...  
[gauss.ffii.org/PatentView/EP1496637](http://gauss.ffii.org/PatentView/EP1496637) - 122k - [Cached](#) - [Similar pages](#)

Simultaneous plural code series generator and CDMA radio receiver ...

The receiver includes a plural **code generator** for simultaneously ... shift stages in which a  
**second initial** value which is distinct from the **first initial** ...  
[www.linkgrinder.com/Patents/Simultaneous\\_pl\\_6728305.html](http://www.linkgrinder.com/Patents/Simultaneous_pl_6728305.html) - 116k - Supplemental Result  
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Data authentication using modification detection codes based on a ...

forming an initial 2N-bit first key value by masking the combination of a **first initial** N bit  
value with a **second initial** N bit value; ...  
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the **code generator** has no control over the register ...  
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**Data transmission method and apparatus using multiple scrambling ...**

The receiving apparatus of claim 9, wherein: the first **code generator** has a ... register to a **first initial** value after a predetermined number of bits, ...

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Al

- ☐ 1. Coding and decoding apparatus for the protection of communication secrecy  
**Martelli, Emilio**, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT,  
Aug 1980  
...signal is transmitted only one time by the transmitting apparatus at the beginning of  
the sequence provided by the relative **code generator** and renders impossible a  
subsequent synchronization with other users desiring to listen. Further, the synchronizing  
signal...  
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printf("**first** value = %u\n", gsl\_rng\_get (r)) return...a.out generator type: mt19937  
seed = 0 **first** value = 2867219139 By setting the two...  
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...7) k = 1 since the **second** term averages to zero...energy E , of duration T **seconds**.  
This signal is one-dimensional...t ) p ( t ) , the **first** term may be extracted...frequencies  
every TH **seconds**, the total bandwidth...from a short segment. **Linear feedback shift**  
**register** (LFSR) sequences [4...  
[http://pdos.csail.mit.edu/decouto/papers/pickholtz82.p...]  
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module 90 **First** interleaving 92 **Second** interleaving 94 **First** De-interleaving 96 **Second** De-Interleaving 98 TFCI Bits Generation 100 BPSK Modulation 103 QPSK Modulation 105...

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Wireless connections made easy Specification of the Bluetooth System v1.0 B December 1st 1999 Specification Volume 1 Core 3 Date / Day-Month-Year Status Document No. Responsible e-mail address N.B. 01 Dec 99 page 3 of 1082 BLUETOOTH DOC Specification of the Bluetooth System Version 1.0 B 1.C.47/1.

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☐ 10. [Serial Attached SCSI Standard](#) [PDF-2MB]

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☐ 11. [GNU Scientific Library -- Reference Manual - Random Number Generation](#) [46K]

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☐ 12. [GNU Scientific Library -- Reference Manual - Random Number Generation](#) [45K]



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...Technology iii ACKNOWLEDGEMENTS **First** of all, I would like to express...67 GPS P-**code Generator** Architecture...67 4.1 P-**code Generator** Architecture...69 4.2 GPS P-**code Generator** Tuning Model...  
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...mega chips per **second**) and 3.6864 Mcps...transmitted in the **first**  $1152 \times N$  chip and...these channels. The **first** three blocks append...encoder returns to the **initial state** at the end of the...section describes **first** the manner in which...through 6. The long **code generator**, which will be discussed...  
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- ☐ **16. Method for updating a linear feedback shift register of code generator**  
**Nieminen, Esko, EUROPEAN PATENT APPLICATION**, Nov 2000  
...means of a **code generator**, for example...generator using a **linear feedback shift register**. WO Publication...**register**. The **initial state** is always...the known **initial state** to a new...how each **initial state** element needs...number of **linear feedback shift register** elements...**register** of a **code generator** to a target...  
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- ☐ **17. Method for updating linear feedback shift register of code generator**  
**Lipponen, Veli / Moilanen, Pentti, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT**, Oct 2003  
...spreading **code generator** 260A, 260B...element of the **initial state** should be...onward using a **linear feedback shift register**. The required...indicate the **initial state** values of the **linear feedback shift register** and the ordinal...using the **linear feedback shift register** described...output of the **first** element 272...input of the **second** element 274...set as the **initial state** with the...  
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- ☐ **18. METHOD FOR UPDATING LINEAR FEEDBACK SHIFT REGISTER OF CODE GENERATOR**  
**LIPPONEN, Veli / MOILANEN, Pentti, PATENT COOPERATION TREATY APPLICATION**, Sep 2000  
...generated with a **code generator**, with a **code generator** using a **linear feedback shift register**, for instance...register. The **initial state** is always...spreading **code generator** 260A, 260B...element of the **initial state** should be...onward using a **linear feedback shift register**. The required...  
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19. Method for updating a linear feedback shift register of code generator  
**Nieminen, Esko**, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Dec 2003  
...spreading **code generator** 260A, 260B...how each **initial state** element must...memory, or the **linear feedback shift register** is used to...elements of the **linear feedback shift register**, and the...from the **initial state**. FIG. 2C...Galois-type **linear feedback shift register**, but it is...output of the **first** element 272...input of the **second** element 274...  
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20. Method for updating linear feedback shift register of code generator  
**Lipponen, Veli E. / Moilanen, Pentti**, UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION, Mar 2002  
...spreading **code generator** 260A, 260B...element of the **initial state** should be...onward using a **linear feedback shift register**. [0054] The...indicate the **initial state** values of the **linear feedback shift register** and the ordinal...using the **linear feedback shift register** described...output of the **first** element 272...input of the **second** element 274...set as the **initial state** with the...  
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IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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## » Key

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IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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- ☐ 2. **Code acquisition scheme for frequency hopping radio in channels with fa**  
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